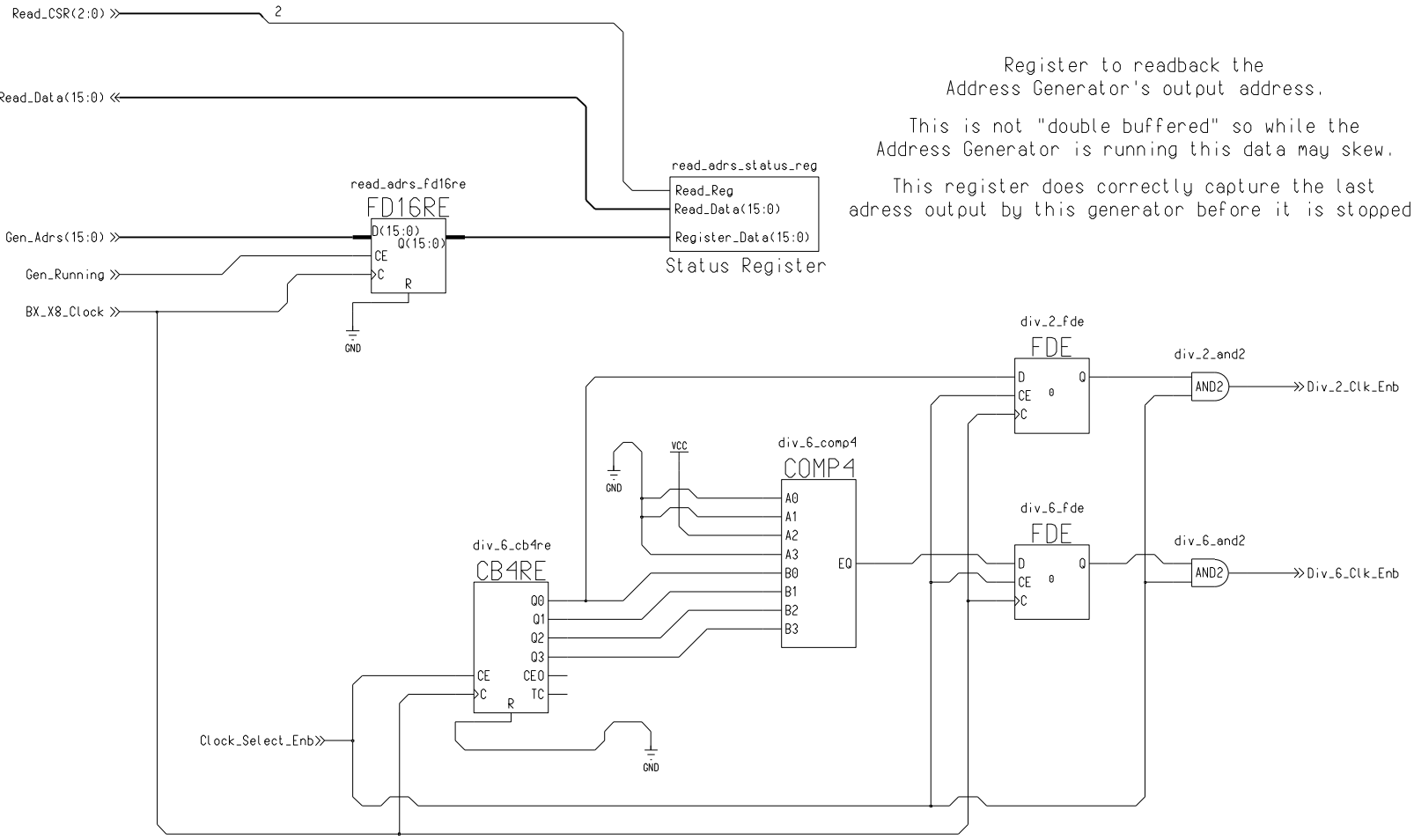

Register to readback the
Address Generator's output address.

This is not "double buffered" so while the
Address Generator is running this data may skew.

This register does correctly capture the last
adress output by this generator before it is stopped.

Divide by 6 so that the Address will only Increment
on every 6th edge of the 8x_Clock that is selected by
Clock_Select_Enb. The Comp Ref is 4 for divide by 6.

Engineer: Librarian	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger
Drawn by: Librarian	
R&D CHK:	TITLE: mem_a_port_adrs_ctrl Memory "A" Port Write Address and Global Control for T7
DOC CTRL CHK:	Size: C
MFG ENGR CHK:	4144

Changed by:	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page:
Edmunds	Monday 16-JAN-2006	12:00:00				1