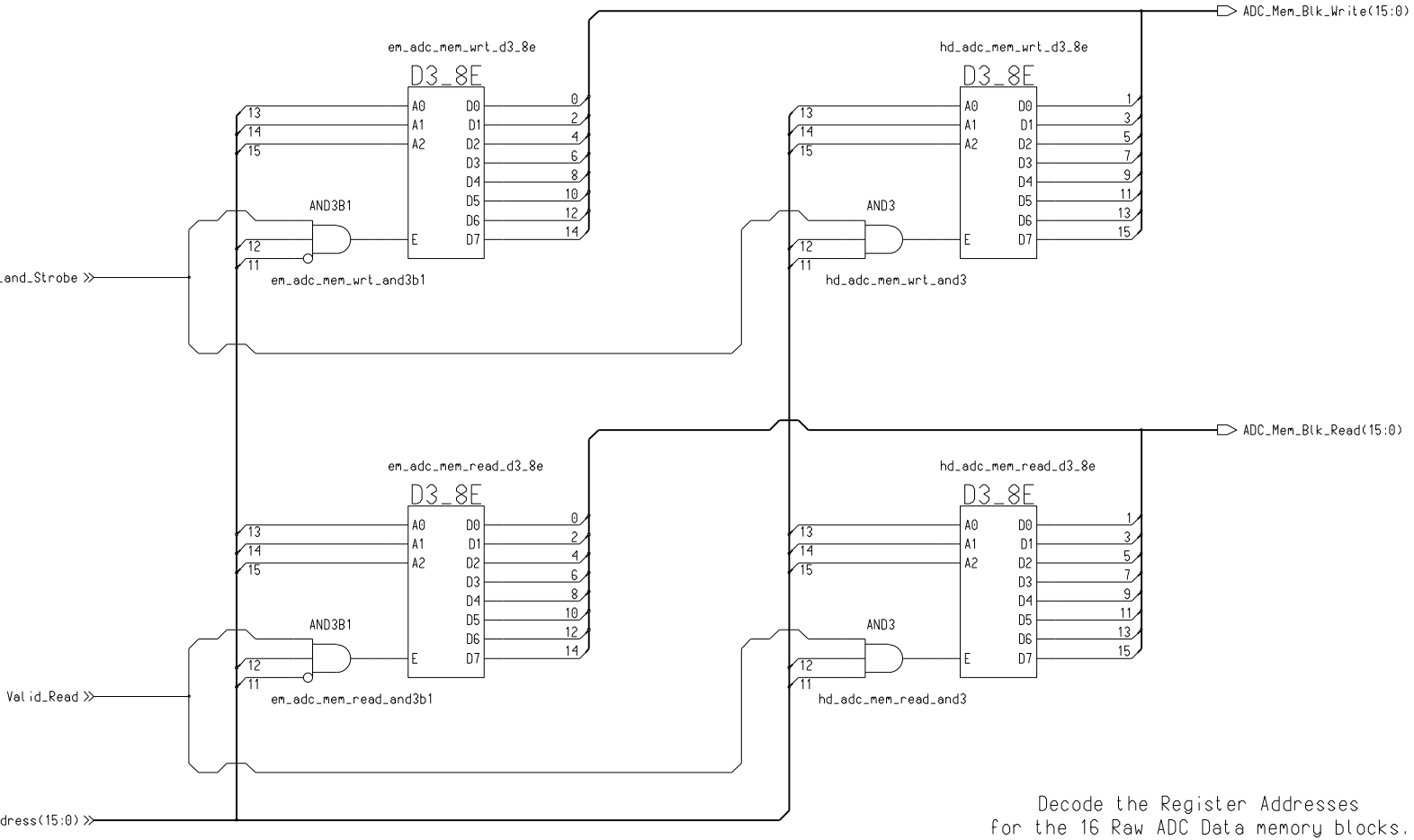

Decode the Register Addresses
for the 16 Raw ADC Data memory blocks.
These are 2K Reg Adrs memory blocks.
This ADC Mem Blk is decoded into Et LU Adrs space.

Engineer: Librarian	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger
Drawn by: Librarian	
R&D CHK:	TITLE: adf2_ocb_if for LArTPC Virtex II OCB Interface T7 Sheet 2 of 6
DOC CTRL CHK:	Size: C
MFG ENGR CHK:	4.1.4.A

Changed by:	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page:
Edmunds	Monday 15-JAN-2006	12:00:00				2