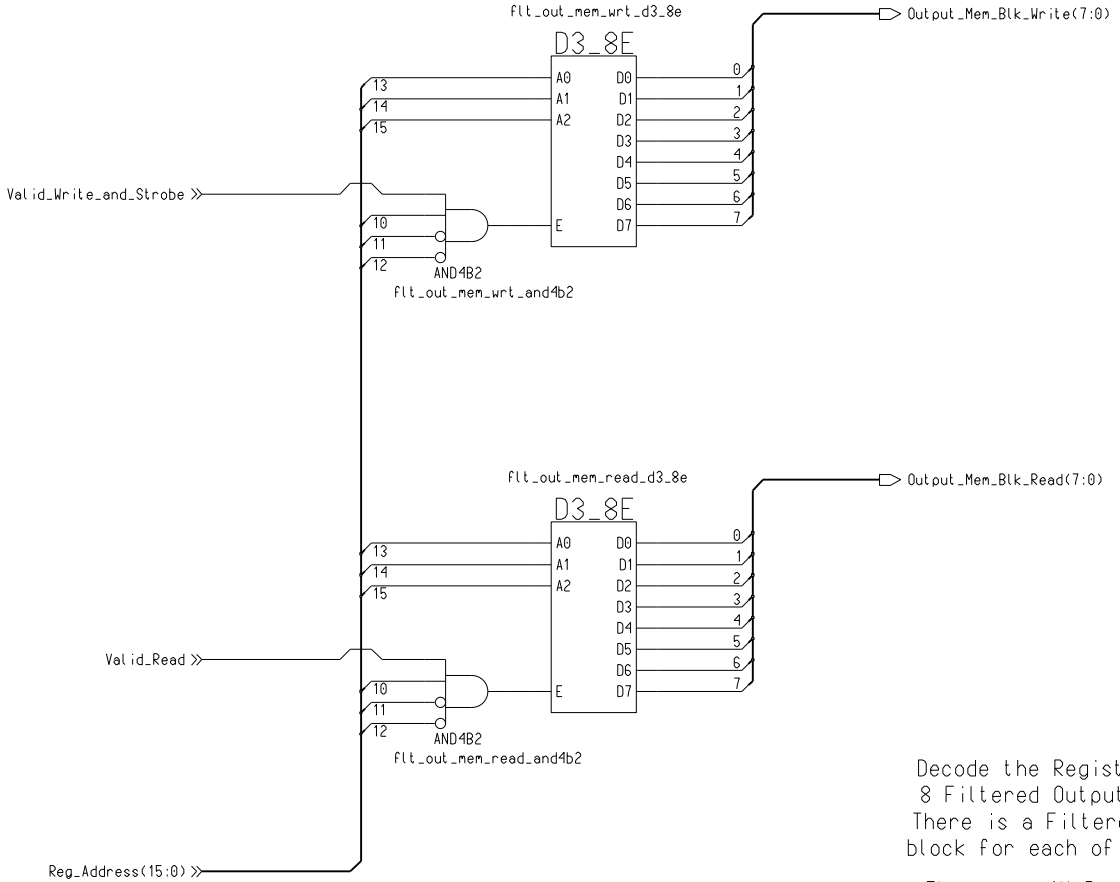

Decode the Register Addresses for the 8 Filtered Output Data memory blocks. There is a Filtered Output Data memory block for each of the 8 Trigger Towers.

These are 1K Reg Adrs memory blocks.

Engineer: Librarian	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger	
Drawn by: Librarian		
R&D CHK:	TITLE: adf2_ocb_if Virtex II OCB Interface T7 Sheet 5 of 6	Size: C 4 1 4 A
DOC CTRL CHK:		
MFG ENGR CHK:		

Changed by:	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page:
Edmunds	Wednesday 6-APR-2005	12:00:00				5