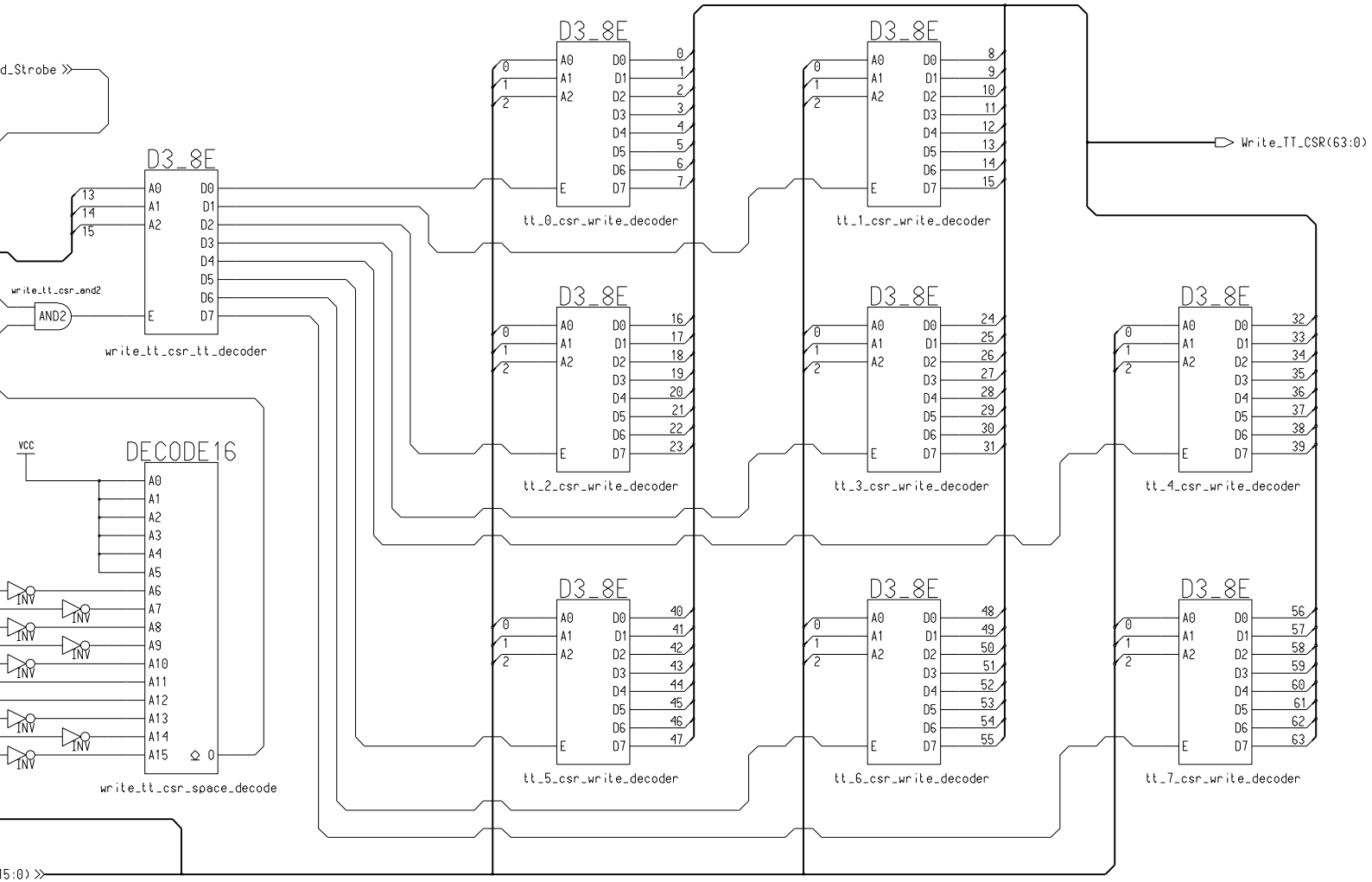

Decode Register Addresses
to Write 8 CSR's from each of
8 Trigger Tower EM CSR spaces.

lects the TT.
lects the CSR to Write.

RA15	RA14	RA13	RA12	RA11	RS10	RA9	RA8	RA7	RA6	RA5	RA4	RA3	RA2	RA1	RA0
X	X	X	0	0	0	1	1	0	0	0	0	0	Y	Y	Y

Changed by: Edmunds	Date Changed: Monday 18-APR-2005	Time Changed: 12:00:00	QA CHK:	REV:	Drawing Number:

Page: 6

Engineer: Librarian
Drawn by: Librarian
R&D CHK:
DOC CTRL CHK:
MFG ENGR CHK:

MICHIGAN STATE UNIVERSITY
D0 Run II Upgrade
Run IIB L1 Calorimeter Trigger

TITLE:
adf2_ocb_if
Virtex II OCB Interface T7
Sheet 6 of 6

Size: C

4 1 4 A