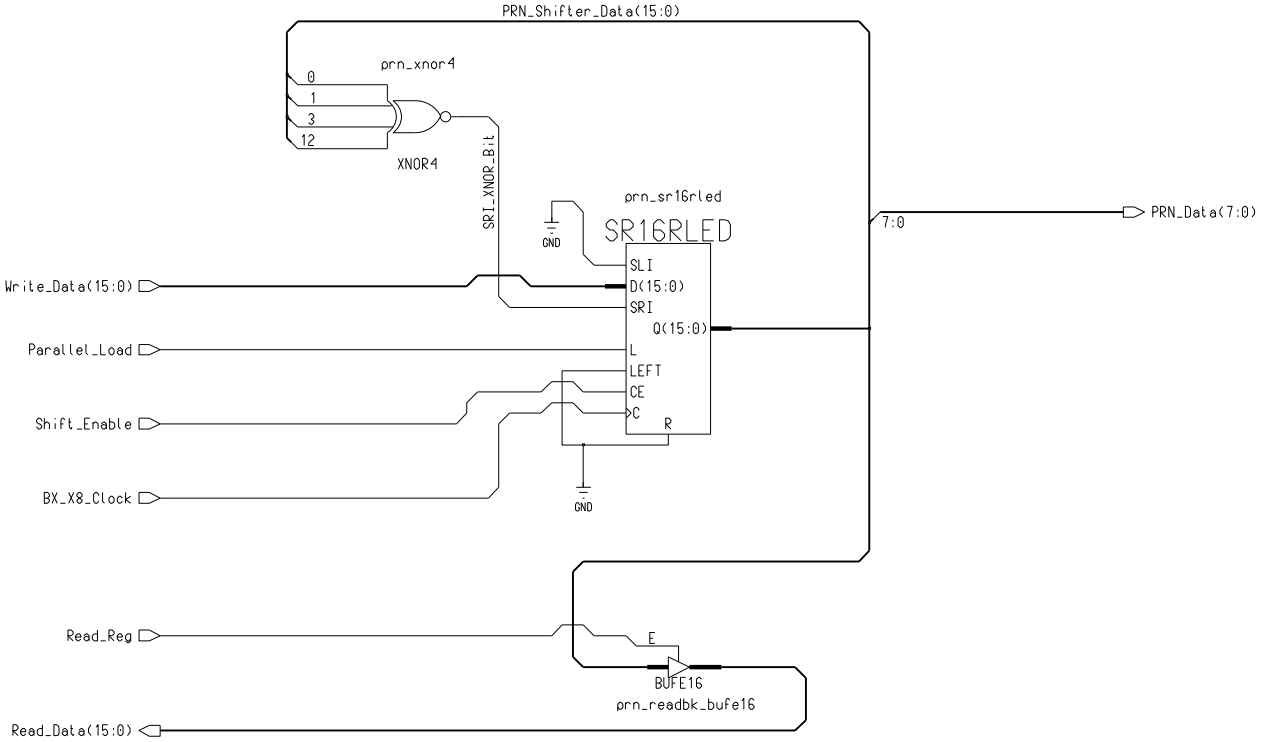

T7 Pseudo Random Noise Generator

XNOR feedback from bits: D0, D1, D3, D12.

For reference see: Xilinx Application Note 052.

The SR16RLED component is used to implement a Right Shift operation, i.e. Q0 shifts out first and then Q1 and then Q2, ...

For the PRN Gen. we need the LSB to shift out first.

The SRI, Shift Right Input, data loads into D15.

In the SR16RLED the LOAD operation overrides the SHIFT operation. Clock Enable is not required for the LOAD operation.

All operations are synchronous and there is a Shift operation on every cycle of the BX_X8_Clock.

Changed by: Edmunds	Date Changed: Monday 24-JAN-2005	Time Changed: 12:00:00	QA CHK:	REV:	Drawing Number: / Page: 1

Engineer: Librarian
Drawn by: Librarian
R&D CHK:
DOC CTRL CHK:
MFG ENGR CHK:

MICHIGAN STATE UNIVERSITY
D0 Run II Upgrade
Run IIB L1 Calorimeter Trigger

TITLE:
prn generator
Pseudo Random Noise Generator
D.P. FPGA Design T7

Size: C

4144