MICHELANGELO STATE UNIVERSITY

00-Page Design 16
One line of channel link output
Shift for one line

TITLE:

CHANGE BY:

DATE CHANGED:

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In every cycle of the BYX-clock,
All operations are synchronous and there is no operation.

Clock Enable is not required for the LOAD operation.
In the SPARE (the LOAD operation overrules the SHIFT operation)

This is accomplished by loading the parahedral gate
For ADD 1 and 2, we need the LSB to shift out.

The SPARE is a left shift component.

So that the LSB comes out first.
Reverse the order of the loaded data.