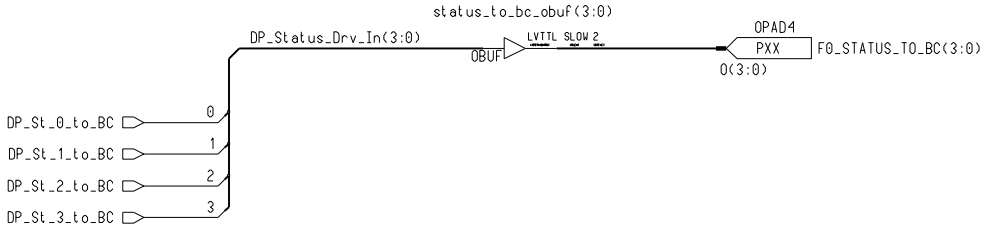

This component is the driver for
the 4 lines that carry Status
information from the Data Path FPGA
to the Board Control PAL

Changed by: Edmunds	Date Changed: Thursday 28-APR-2005	Time Changed: 12:00:00	QA CHK:	REV:	Drawing Number:	Page: 1

Engineer: Librarian	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger
Drawn by: Librarian	
R&D CHK:	TITLE: status_to_bc_pal DP FPGA Status to the BC PAL T7
DOC CTRL CHK:	Size: C
MFG ENGR CHK:	4 1 4 A