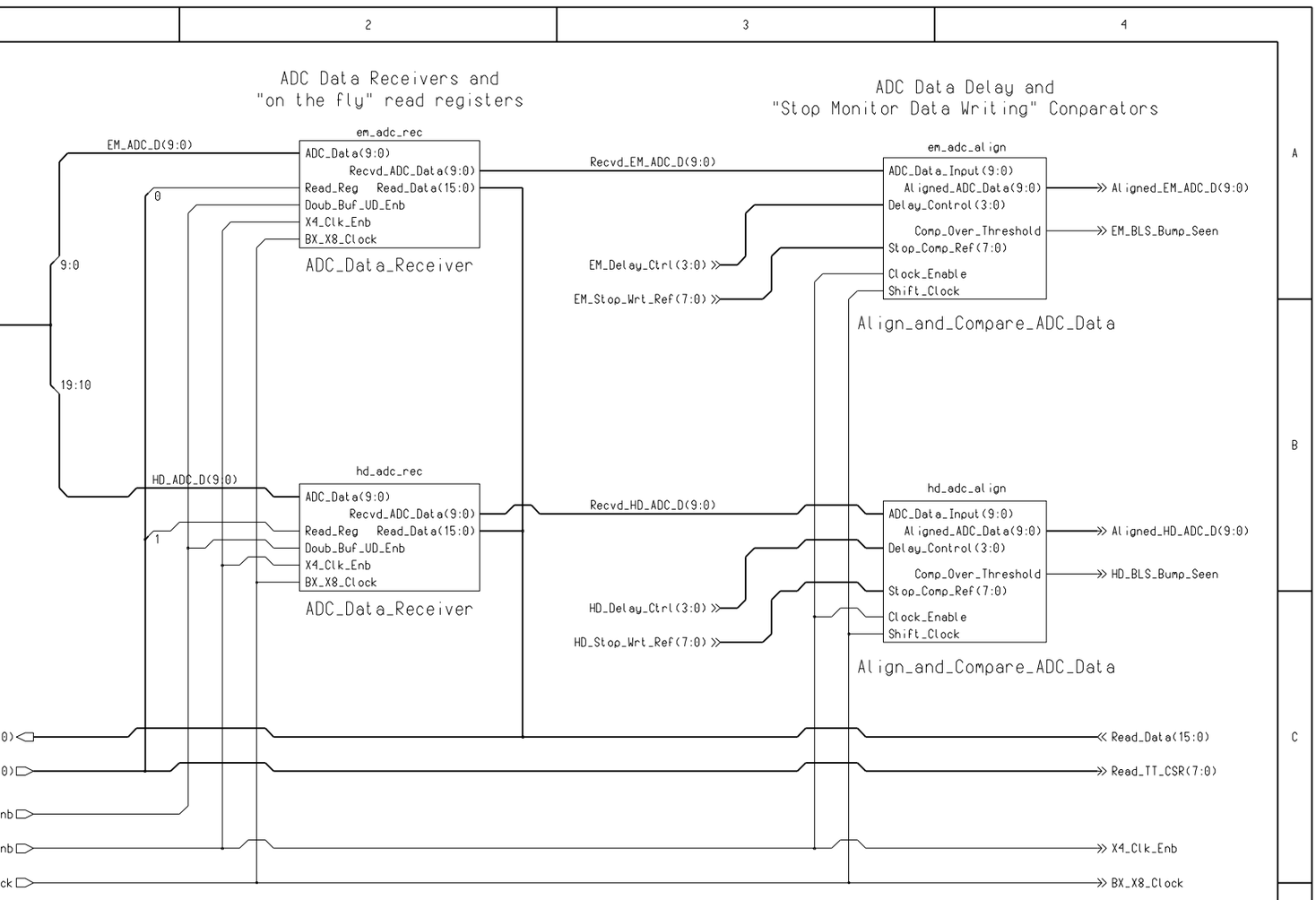

Trigger Tower Cell of Data Path Page 1 of 9

Receive ADC Data in IOB D-Latch.
Provide "on the fly" VME read of ADC data.
Align ADC Data to compensate for BLS signal skew.
Compare ADC Data to Stop Monitor Data Write Threshold.

Engineer: Edmunds	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger
Drawn by: Librarian	
R&D CHK:	TITLE: tt_cell Trigger Tower Cell of Data Path D.P. FPGA Design T7
DOC CTRL CHK:	Size: C
MFG ENGR CHK:	4114A
Changed by: Edmunds	Date Changed: Wednesday 11-MAY-2005
Time Changed: 12:00:00	QA CHK:
REV:	Drawing Number:
Page: 1	