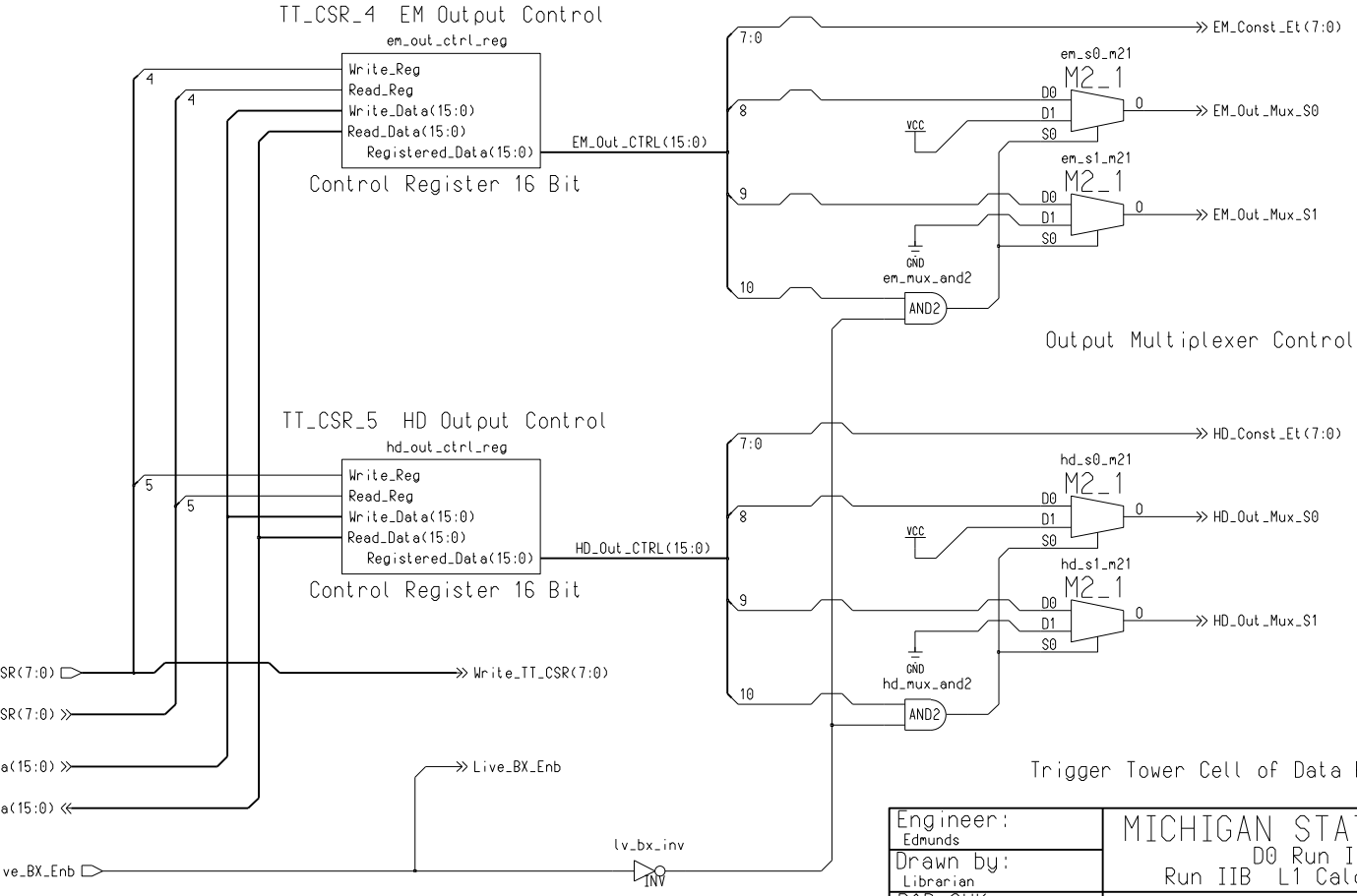

Trigger Tower Cell of Data Path Page 4 of 9

TT Output Section Control

Control of the Constant Et Value and of the Output Multiplexer
Control of the Output Mem Blk A-Port Write Enable.

Changed by: Edmunds	Date Changed: Friday 13-JAN-2006	Time Changed: 12:00:00	QA CHK:	REV:	Drawing Number: 4 1 4 A	Page: 4

Engineer: Edmunds	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger
Drawn by: Librarian	
R&D CHK:	TITLE: tt_cell for LArTPC Trigger Tower Cell of Data Path D.P. FPGA Design T7
DOC CTRL CHK:	Size: C
MFG ENGR CHK:	