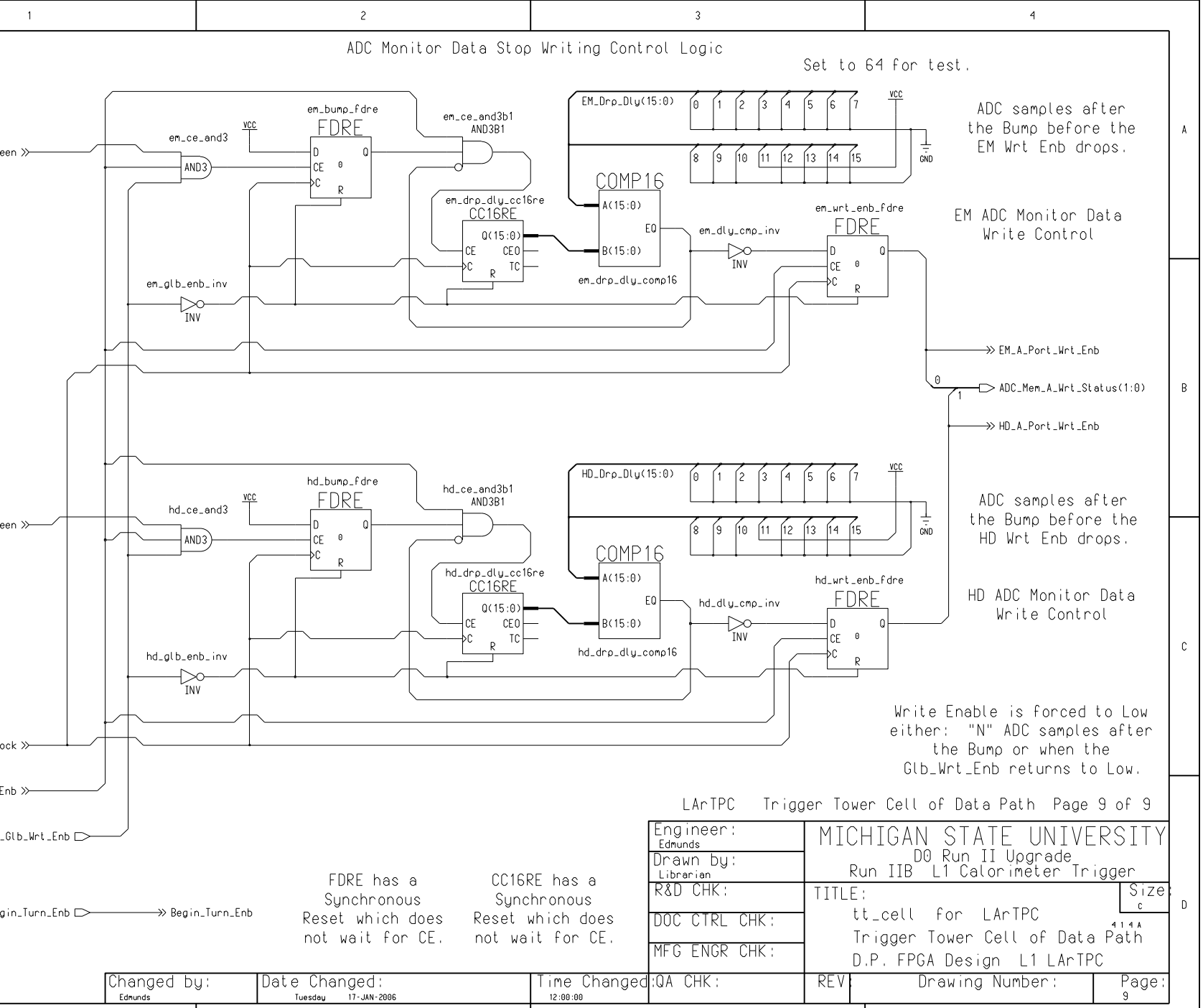

LArTPC Trigger Tower Cell of Data Path Page 9 of 9

Engineer: Edmunds	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger	
Drawn by: Librarian		
R&D CHK:	TITLE: tt_cell for LArTPC Trigger Tower Cell of Data Path D.P. FPGA Design L1 LArTPC	Size: C
DOC CTRL CHK:		
MFG ENGR CHK:		

Changed by:	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page:
Edmunds	Tuesday 17-JAN-2006	12:00:00			4144	9