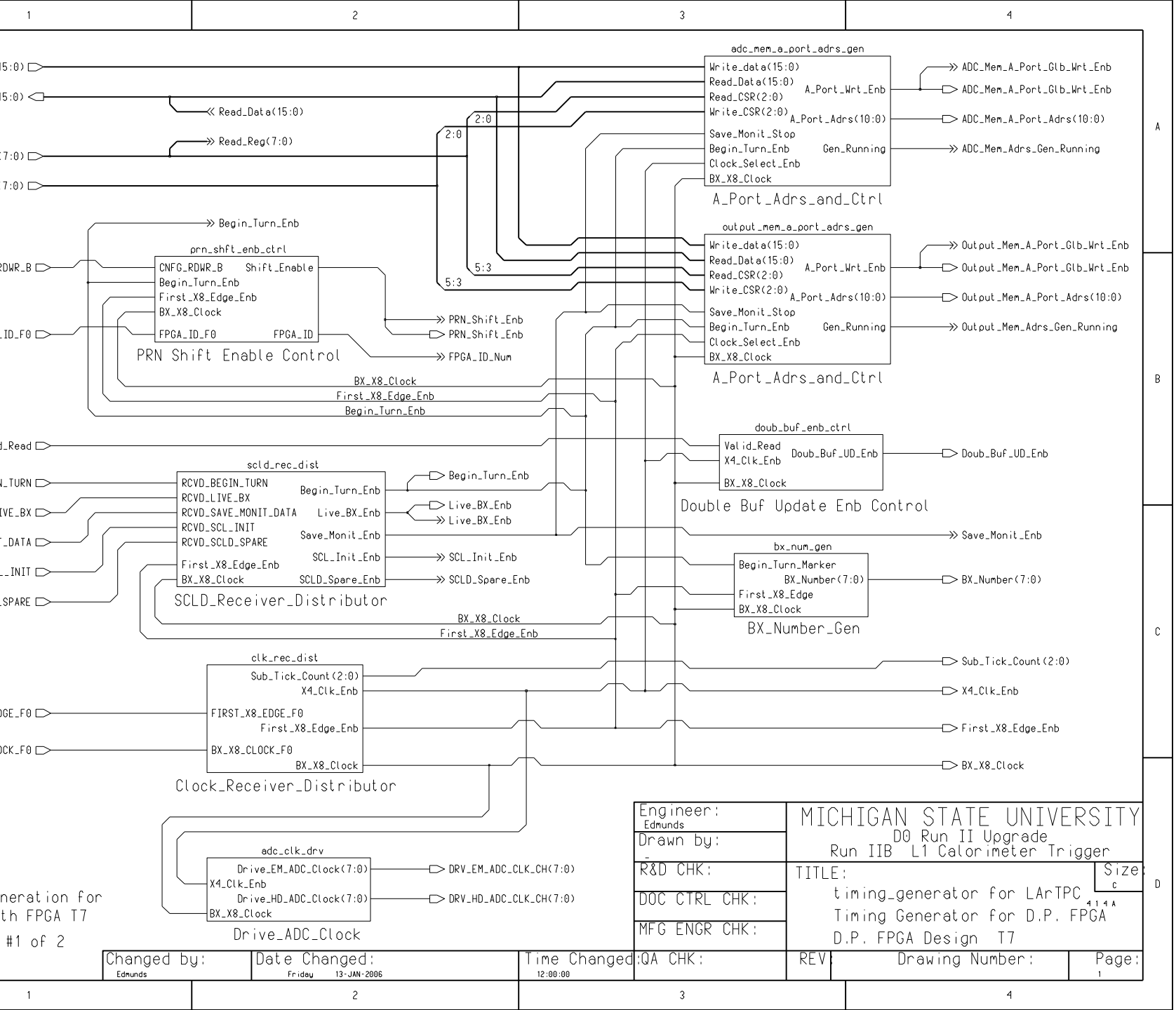
adc_mem_a_port_adrs_gen
Write_data(15:0)
Read_Data(15:0)
Read_CSR(2:0)
Write_CSR(2:0)
Save_Monit_Stop
Begin_Turn_Enb
Clock_Select_Enb
BX_X8_Clock
A_Port_Wrt_Enb
A_Port_Adrs(10:0)
Gen_Running
ADC_Mem_A_Port_Glb_Wrt_Enb
ADC_Mem_A_Port_Glb_Wrt_Enb
ADC_Mem_A_Port_Adrs(10:0)
ADC_Mem_Adrs_Gen_Running
A_Port_Adrs_and_Ctrl
Read_Data(15:0)
Read_Reg(7:0)
2:0
2:0
5:3
5:3
Begin_Turn_Enb
prn_shft_enb_ctrl
CNFG_RDWR_B
Begin_Turn_Enb
First_X8_Edge_Enb
BX_X8_Clock
FPGA_ID_F0
Shift_Enable
FPGA_ID
PRN_Shift_Enb
PRN_Shift_Enb
FPGA_ID_Num
PRN Shift Enable Control
output_mem_a_port_adrs_gen
Write_data(15:0)
Read_Data(15:0)
Read_CSR(2:0)
Write_CSR(2:0)
Save_Monit_Stop
Begin_Turn_Enb
Clock_Select_Enb
BX_X8_Clock
A_Port_Wrt_Enb
A_Port_Adrs(10:0)
Gen_Running
Output_Mem_A_Port_Glb_Wrt_Enb
Output_Mem_A_Port_Glb_Wrt_Enb
Output_Mem_A_Port_Adrs(10:0)
Output_Mem_Adrs_Gen_Running
A_Port_Adrs_and_Ctrl
BX_X8_Clock
First_X8_Edge_Enb
Begin_Turn_Enb
doub_buf_enb_ctrl
Valid_Read
X4_Clk_Enb
BX_X8_Clock
Doub_Buf_UD_Enb
Doub_Buf_UD_Enb
Double Buf Update Enb Control
scld_rec_dist
RCVD_BEGIN_TURN
RCVD_LIVE_BX
RCVD_SAVE_MONIT_DATA
RCVD_SCL_INIT
RCVD_SCLD_SPARE
First_X8_Edge_Enb
BX_X8_Clock
Begin_Turn_Enb
Live_BX_Enb
Save_Monit_Enb
SCL_Init_Enb
SCLD_Spare_Enb
Begin_Turn_Enb
Live_BX_Enb
Live_BX_Enb
SCL_Init_Enb
SCLD_Spare_Enb
SCLD_Receiver_Distributor
Save_Monit_Enb
bx_num_gen
Begin_Turn_Marker
First_X8_Edge
BX_X8_Clock
BX_Number(7:0)
BX_Number(7:0)
BX_Number_Gen
BX_X8_Clock
First_X8_Edge_Enb
clk_rec_dist
Sub_Tick_Count(2:0)
X4_Clk_Enb
FIRST_X8_EDGE_F0
First_X8_Edge_Enb
BX_X8_CLOCK_F0
BX_X8_Clock
Sub_Tick_Count(2:0)
X4_Clk_Enb
First_X8_Edge_Enb
BX_X8_Clock
Clock_Receiver_Distributor
adc_clk_drv
Drive_EM_ADC_Clock(7:0)
X4_Clk_Enb
Drive_HD_ADC_Clock(7:0)
BX_X8_Clock
DRV_EM_ADC_CLK_CH(7:0)
DRV_HD_ADC_CLK_CH(7:0)
Drive_ADC_Clock
Engineer: Edmunds
Drawn by: -
R&D CHK:
DOC CTRL CHK:
MFG ENGR CHK:
MICHIGAN STATE UNIVERSITY
D0 Run II Upgrade
Run IIB L1 Calorimeter Trigger
TITLE:
timing_generator for LArTPC
Timing Generator for D.P. FPGA
D.P. FPGA Design T7
Size: C
Changed by: Edmunds
Date Changed: Friday 13-JAN-2006
Time Changed: 12:00:00
QA CHK:
REV:
Drawing Number:
Page: 1