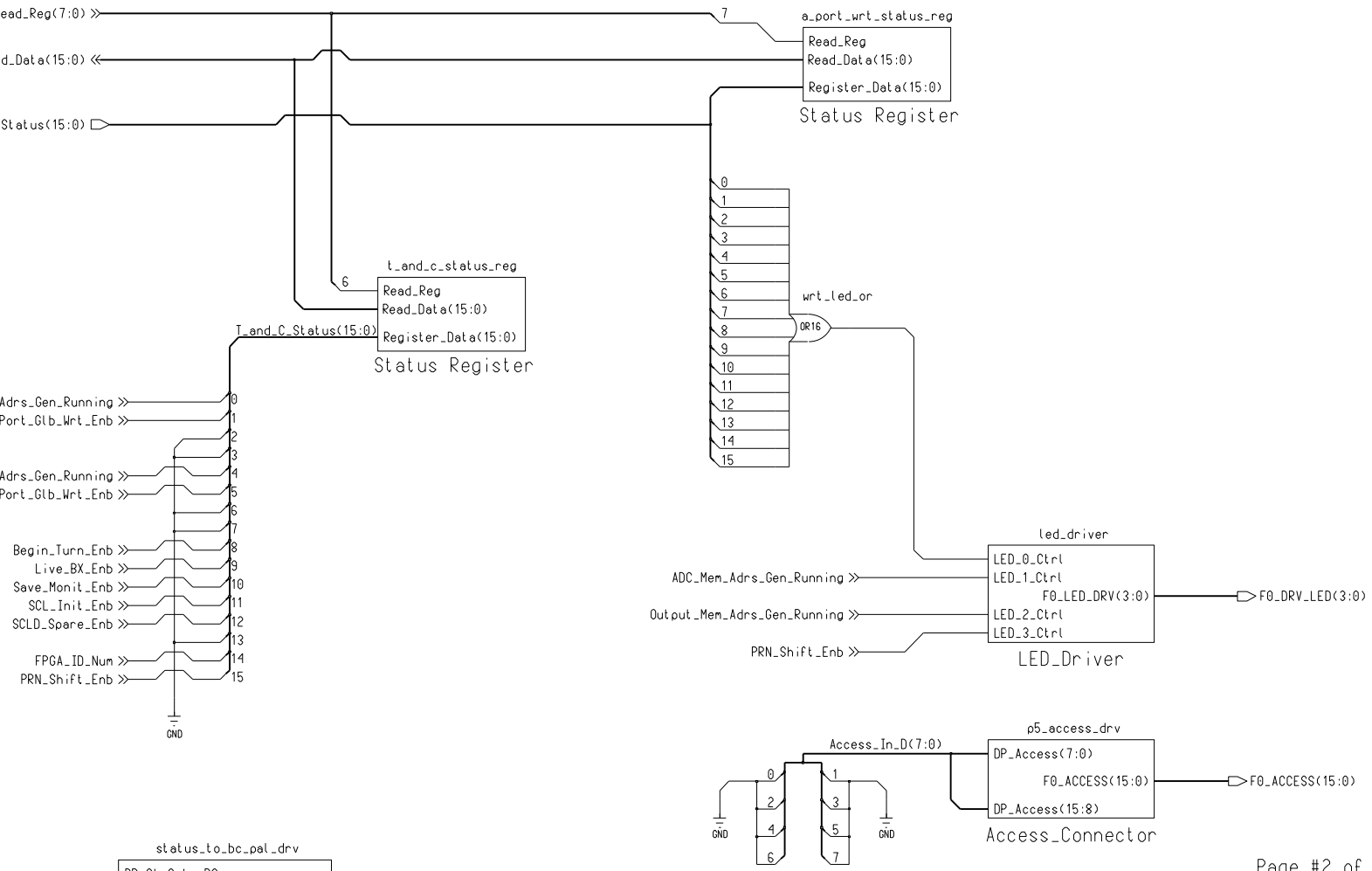

Page #2 of 2

Timing Generation for
Data Path FPGA T7

Changed by: Edmunds	Date Changed: Thursday 12-MAY-2005	Time Changed: 12:00:00	QA CHK:	REV:	Drawing Number:	Page: 2

Engineer: Edmunds	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger
Drawn by: -	
R&D CHK:	TITLE: timing_generator Timing Generator for D.P. FPGA Page #2 of 2 D.P. FPGA T7
DOC CTRL CHK:	Size: C
MFG ENGR CHK:	4 1 4 A