

1

2

3

4

A

A

B

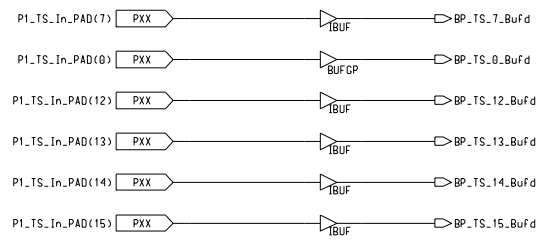
B

C

C

D

D



Engineer: Edmunds	MICHIGAN STATE UNIVERSITY 00 Run II Upgrade Level 1/Level 2 Trigger Frameworks
Drawn by: Edmunds	
R&D CHK:	TITLE:
DOC CTRL CHK:	Backplane TS Input Buffer
MFG ENGR CHK:	Fast IR Camera BSF FPGA

Size c
411A

Changed by: edmunds	Date Changed: Tuesday, November 18, 1997	Time Changed: 9:04:39 am	QA CHK:	REV	Drawing Number:	Page: 1
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1

2

3

4