

1

2

3

4

A

A

B

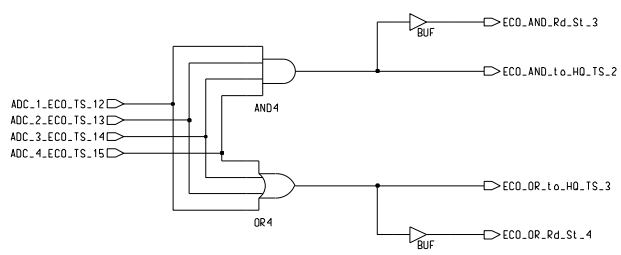
B

C

C

D

D



Engineer: Edmunds	MICHIGAN STATE UNIVERSITY 00 Run II Upgrade Level 1/Level 2 Trigger Frameworks	Size c 21" x 28"
Drawn by: Edmunds		
R&D CHK:	TITLE:	
DOC CTRL CHK:	EOC End of Conversion Gates	
MFG ENGR CHK:	Fast IR Camera BSF FPGA	

Changed by: edmunds	Date Changed: Monday, November 17, 1997	Time Changed: 11:21:05 am	QA CHK:	REV	Drawing Number:	Page: 1
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1

2

3

4