



Engineer: Edmunds	MICHIGAN STATE UNIVERSITY 00 Run II Upgrade Level 1/Level 2 Trigger Frameworks
Drawn by: Edmunds	
R&D CHK:	TITLE: Integrate_Control
DOC CTRL CHK:	FIRC BSF FPGA
MFG ENGR CHK:	

Changed by: Edmunds	Date Changed: Tuesday, November 18, 1997	Time Changed: 9:41:42 am	QA CHK:	REV	Drawing Number:	Page: 1
------------------------	---	-----------------------------	---------	-----	-----------------	------------