



Engineer: Edmunds	MICHIGAN STATE UNIVERSITY 00 Run II Upgrade Level 1/Level 2 Trigger Frameworks
Drawn by: Edmunds	
R&D CHK:	TITLE: Fast IR Camera BSF FPGA
DOC CTRL CHK:	Size 4144
MFG ENGR CHK:	Global IO and Control Status Register

Changed by: Edmunds	Date Changed: Thursday, November 20, 1997	Time Changed: 10:31:54 am	QA CHK:	REV	Drawing Number:	Page: 1
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