



Engineer: Edmunds	MICHIGAN STATE UNIVERSITY 00 Run II Upgrade Level 1/Level 2 Trigger Frameworks	Size 41.44
Drawn by: Edmunds		
R&D CHK:	TITLE: Control & Status Register	
DOC CTRL CHK:	FIRC MSA #4 FPGA	
MFG ENGR CHK:		

Changed by: Edmunds	Date Changed: Saturday, November 29, 1997	Time Changed: 2:59:16 pm	QA CHK:	REV	Drawing Number:	Page: 1
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