



Engineer: Edmunds	MICHIGAN STATE UNIVERSITY	
Drawn by: Edmunds	00 Run II Upgrade Level 1/Level 2 Trigger Frameworks	
R&D CHK:	TITLE:	Size c
DOC CTRL CHK:	MSA ADC Data Mux FIRC MSA #4 FPGA	
MFG ENGR CHK:	REV	414A
Changed by: Edmunds	Date Changed: Saturday, November 29, 1997	Time Changed: 5:20:28 am
QA CHK:	REV	Page: 1