



LE_ADC_CNT and LE_CNT_CLR
MSA Output (31:24) and MSA Output (23)
can be used for testing
the Line Enable Block

16 Bit MSA
Output Buffer
To_P4(31:16)
MSA_Out_Pad(31:16)
MSA_Out_Pad_Buf

Engineer:	MSU HEP		
Drawn by:	MSU HEP		
R&D CHK:	MICHIGAN STATE UNIVERSITY		
DOC CTRL CHK:	DO Run II Upgrade		
MFG ENGR CHK:	Level 1/Level 2 Trigger Frameworks		
QA CHK:	TITLE:	FIRC MSA #4 FPGA	Size c
REV:	Top-Level Block Diagram		414A
REV:	Drawing Number:	Page:	
REV:	1	1	

Changed by:	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page:
Edmunds	Saturday, December 20, 1997	2:14:00 pm				1