Description	Resource	Path	Location	Type
Unused port: D_CBL_77_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 687 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_62_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 672 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_60_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 670 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_73_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 683 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Deprecated package	daq_collector.vhd	/CMX_project/Jet/Jet_common/trunk/sources	line: 14 /CMX_project/Jet/Jet_common/trunk/sources/daq_collector.vhd	VHDL Problem
Deprecated package	daq_collector.vhd	/CMX_project/Jet/Jet_common/trunk/sources	line: 15 /CMX_project/Jet/Jet_common/trunk/sources/daq_collector.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	daq_collector.vhd	/CMX_project/Jet/Jet_common/trunk/sources	line: 17 /CMX_project/Jet/Jet_common/trunk/sources/daq_collector.vhd	VHDL Problem
Unused port: D_CBL_81_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 633 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_72_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 682 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	or_all.vhd	/CMX_project/Common/trunk/sources	line: 15 /CMX_project/Common/trunk/sources/or_all.vhd	VHDL Problem
Superfluous signals in sensitivity list. The process is not sensitive to signal "wen"	vme_inreg_rtl_notri_async.vhd	/CMX_project/Common/trunk/sources	line: 71 /CMX_project/Common/trunk/sources/vme_inreg_rtl_notri_async.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	CMX_cable_clocked_80Mbps_input_module.vhd	/CMX_project/Common/trunk/sources	line: 18 /CMX_project/Common/trunk/sources/CMX_cable_clocked_80Mbps_input_module.vhd	VHDL Problem
Unused port: buf_clk200 is not used in work.CMX_cable_clocked_80Mbps_input_module(Behavioral)	CMX_cable_clocked_80Mbps_input_module.vhd	/CMX_project/Common/trunk/sources	line: 31 /CMX_project/Common/trunk/sources/CMX_cable_clocked_80Mbps_input_module.vhd	VHDL Problem
Unused port: del_array is not used in work.CMX_cable_clocked_80Mbps_input_module(Behavioral)	CMX_cable_clocked_80Mbps_input_module.vhd	/CMX_project/Common/trunk/sources	line: 33 /CMX_project/Common/trunk/sources/CMX_cable_clocked_80Mbps_input_module.vhd	VHDL Problem
Unused port: buf_clk40 is not used in work.CMX_cable_clocked_80Mbps_input_module(Behavioral)	CMX_cable_clocked_80Mbps_input_module.vhd	/CMX_project/Common/trunk/sources	line: 30 /CMX_project/Common/trunk/sources/CMX_cable_clocked_80Mbps_input_module.vhd	VHDL Problem
Unused port: upload_delays is not used in work.CMX_cable_clocked_80Mbps_input_module(Behavioral)	CMX_cable_clocked_80Mbps_input_module.vhd	/CMX_project/Common/trunk/sources	line: 34 /CMX_project/Common/trunk/sources/CMX_cable_clocked_80Mbps_input_module.vhd	VHDL Problem
Unused port: D_CBL_54_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 664 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: BF_TO_TP_DAQ_SLINK_RETURN_DIR is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 693 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 797 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 816 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	adder_top_vs.vhd	/CMX_project/Jet/Jet_common/trunk/sources	line: 22 /CMX_project/Jet/Jet_common/trunk/sources/adder_top_vs.vhd	VHDL Problem
Unused port: D_CBL_71_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 681 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_64_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 674 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_63_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 673 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_80_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 690 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: BF_TO_TP_DAQ_SLINK_RETURN_CMP is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 694 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: BF_TO_TP_ROI_SLINK_RETURN_CMP is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 696 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	CMX_CTP_output_module.vhd	/CMX_project/Common/trunk/sources	line: 21 /CMX_project/Common/trunk/sources/CMX_CTP_output_module.vhd	VHDL Problem
Unused port: D_CBL_79_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 689 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: buf_clk200 is not used in work.CMX_CTP_output_module(Behavioral)	CMX_CTP_output_module.vhd	/CMX_project/Common/trunk/sources	line: 38 /CMX_project/Common/trunk/sources/CMX_CTP_output_module.vhd	VHDL Problem
Unused port: D_CBL_75_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 685 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: BF_TO_TP_ROI_SLINK_RETURN_DIR is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 695 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_82_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 662 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_76_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 686 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_67_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 677 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	CMX_rate_counter_inhibit.vhd	/CMX_project/Common/trunk/sources	line: 15 /CMX_project/Common/trunk/sources/CMX_rate_counter_inhibit.vhd	VHDL Problem
Unused port: D_CBL_58_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 668 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: OCB_SYS_RESET_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 60 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_83_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 691 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_68_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 678 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: clk40 is not used in work.CMX_version(Behavioral)	CMX_version.vhd	/CMX_project/Common/trunk/sources	line: 21 /CMX_project/Common/trunk/sources/CMX_version.vhd	VHDL Problem
Unused port: D_CBL_56_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 666 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: BF_TO_FROM_BSPT_1 is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 529 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_65_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 675 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_66_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 676 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_generic_module_spy_mem_control_FSM.vhd	/CMX_project/Common/trunk/sources	line: 231 /CMX_project/Common/trunk/sources/CMX_generic_module_spy_mem_control_FSM.vhd	VHDL Problem
Unused port: D_CBL_69_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 679 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Case statement contains all choices explicitly. You can safely remove the redundant 'others'	CMX_generic_module_spy_mem_control_FSM.vhd	/CMX_project/Common/trunk/sources	line: 493 /CMX_project/Common/trunk/sources/CMX_generic_module_spy_mem_control_FSM.vhd	VHDL Problem
Unused port: D_CBL_70_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 680 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	CMX_version.vhd	/CMX_project/Common/trunk/sources	line: 12 /CMX_project/Common/trunk/sources/CMX_version.vhd	VHDL Problem
Unused port: D_CBL_78_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 688 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_version.vhd	/CMX_project/Common/trunk/sources	line: 58 /CMX_project/Common/trunk/sources/CMX_version.vhd	VHDL Problem
Unused port: D_CBL_74_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 684 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_57_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 667 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	CMX_Memory_spy_inhibit.vhd	/CMX_project/Common/trunk/sources	line: 23 /CMX_project/Common/trunk/sources/CMX_Memory_spy_inhibit.vhd	VHDL Problem
Unused port: D_CBL_59_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 669 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: D_CBL_55_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 665 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	CMX_generic_module_spy_mem_control_FSM.vhd	/CMX_project/Common/trunk/sources	line: 19 /CMX_project/Common/trunk/sources/CMX_generic_module_spy_mem_control_FSM.vhd	VHDL Problem
Unused port: D_CBL_61_B is not used in work.CMX_top_Base(Behavioral)	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 671 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused port: ds is not used in work.vme_outreg_notri_async(rtl)	vme_outreg_rtl_notri_async.vhd	/CMX_project/Common/trunk/sources	line: 24 /CMX_project/Common/trunk/sources/vme_outreg_rtl_notri_async.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1188 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 15 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Function declarations in a package cannot have a function body. Move the function body to the package body instead	CMXpackage.vhd	/CMX_project/Common/trunk/sources	line: 569 /CMX_project/Common/trunk/sources/CMXpackage.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1205 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1285 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Function declarations in a package cannot have a function body. Move the function body to the package body instead	CMXpackage.vhd	/CMX_project/Common/trunk/sources	line: 549 /CMX_project/Common/trunk/sources/CMXpackage.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1594 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Function declarations in a package cannot have a function body. Move the function body to the package body instead	CMXpackage.vhd	/CMX_project/Common/trunk/sources	line: 556 /CMX_project/Common/trunk/sources/CMXpackage.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1740 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1204 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1632 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1235 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1534 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1595 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1732 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 813 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem
Unused declaration	CMX_top_Base.vhd	/CMX_project/Jet/Jet_system/trunk/sources	line: 1633 /CMX_project/Jet/Jet_system/trunk/sources/CMX_top_Base.vhd	VHDL Problem

Unused port: data_vme_in is not used in work.sys_monitor(Behavioral)	sys_monitor.vhd	/CMX_project/Common/trunk/sources	line: 57 /CMX_project/Common/trunk/sources/sys_monitor.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	CMX_flavor_package.vhd	/CMX_project/Jet/Jet_common/trunk/sources	line: 14 /CMX_project/Jet/Jet_common/trunk/sources/CMX_flavor_package.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	sys_monitor.vhd	/CMX_project/Common/trunk/sources	line: 15 /CMX_project/Common/trunk/sources/sys_monitor.vhd	VHDL Problem
Procedure declarations in a package cannot have a procedure body. Move the procedure body to the package body instead	CMX_flavor_package.vhd	/CMX_project/Jet/Jet_common/trunk/sources	line: 451 /CMX_project/Jet/Jet_common/trunk/sources/CMX_flavor_package.vhd	VHDL Problem
Superfluous library clause: access to library "work" is implicit	CMX_system_cable_input_module.vhd	/CMX_project/Common/trunk/sources	line: 22 /CMX_project/Common/trunk/sources/CMX_system_cable_input_module.vhd	VHDL Problem
Function declarations in a package cannot have a function body. Move the function body to the package body instead	CMX_flavor_package.vhd	/CMX_project/Jet/Jet_common/trunk/sources	line: 463 /CMX_project/Jet/Jet_common/trunk/sources/CMX_flavor_package.vhd	VHDL Problem
Function declarations in a package cannot have a function body. Move the function body to the package body instead	CMXpackage.vhd	/CMX_project/Common/trunk/sources	line: 537 /CMX_project/Common/trunk/sources/CMXpackage.vhd	VHDL Problem
Deprecated package	sys_monitor.vhd	/CMX_project/Common/trunk/sources	line: 11 /CMX_project/Common/trunk/sources/sys_monitor.vhd	VHDL Problem
Deprecated package	sys_monitor.vhd	/CMX_project/Common/trunk/sources	line: 10 /CMX_project/Common/trunk/sources/sys_monitor.vhd	VHDL Problem