

Unused port: OCB_SYS_RESET_B is not used in cp_system.CMX_top_Base(Behavioral)
Unused port: OCB_SYS_RESET_B is not used in jet_crate.CMX_top_Base(Behavioral)
Unused port: OCB_SYS_RESET_B is not used in jet_system.CMX_top_Base(Behavioral)
Unused port: OCB_SYS_RESET_B is not used in sumet_crate.CMX_top_Base(Behavioral)
Unused port: OCB_SYS_RESET_B is not used in sumet_system.CMX_top_Base(Behavioral)
Unused port: p0 locked is not used in cp_common.decoder(RTL)
Unused port: p0 locked is not used in jet_common.decoder(RTL)
Unused port: B0Nep_Q is not used in common.glink.Encoder(rtl)
Unused port: TxCtrl is not used in common.glink.Encoder(rtl)
Unused port: upload_delays is not used in common.CMX_cable_clocked_80Mbps_input_module(Behavioral)

Infos (2)
Non-standard package
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CMX_top_Base.vhd
CMX_top_Base.vhd
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cp_decoder.vhd
jet_decoder.vhd
glink_encoder.vhd
glink_encoder.vhd
CMX_cable_clocked_80Mbps_input_module.vhd

SFP_TXRX.vhd
Topic_Data_TX.vhd

/CMX_project/Base/CP/CP_system/trunk/sources
/CMX_project/Base/jet/jet_crate/trunk/sources
/CMX_project/Base/jet/jet_system/trunk/sources
/CMX_project/Base/SumET/SumET_crate/trunk/sources
/CMX_project/Base/SumET/SumET_system/trunk/sources
/CMX_project/Base/CP/CP_common/trunk/sources
/CMX_project/Base/jet/jet_common/trunk/sources
/CMX_project/Base/Common/trunk/sources
/CMX_project/Base/Common/trunk/sources

/CMX_project/Base/Common/trunk/sources
/CMX_project/Base/Common/trunk/sources

line: 62 /CMX_project/Base/CP/CP_system/trunk/sources/CMX_top_Base.vhd
line: 62 /CMX_project/Base/jet/jet_crate/trunk/sources/CMX_top_Base.vhd
line: 62 /CMX_project/Base/jet/jet_system/trunk/sources/CMX_top_Base.vhd
line: 62 /CMX_project/Base/SumET/SumET_crate/trunk/sources/CMX_top_Base.vhd
line: 62 /CMX_project/Base/SumET/SumET_system/trunk/sources/CMX_top_Base.vhd
line: 37 /CMX_project/Base/CP/CP_common/trunk/sources/cp_decoder.vhd
line: 47 /CMX_project/Base/jet/jet_common/trunk/sources/jet_decoder.vhd
line: 29 /CMX_project/Base/Common/trunk/sources/glink_encoder.vhd
line: 22 /CMX_project/Base/Common/trunk/sources/glink_encoder.vhd
line: 34 /CMX_project/Base/Common/trunk/sources/CMX_cable_clocked_80Mbps_input_module.vhd

line: 10 /CMX_project/Base/Common/trunk/sources/SFP_TXRX.vhd
line: 11 /CMX_project/Base/Common/trunk/sources/Topic_Data_TX.vhd

VHDL Problem this often can not be avoided if one has more pins than actually used - but should be cross checked if they really should not be connected
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VHDL Problem ieee.std_logic_misc is used - one really needs to know what the operators and functions inside do and why there is no standard alternative
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