

VME corrupt write cycle

- Want to write xFFFF to x7811F2 (= x3C08F9 word addr)
- Correct Address and Data appear on the bus (A)
- But then they change to Data=x0000 Addr=x7811F6 (=x3C08FB word addr)!
- And then DS* is asserted! (C)
- Result: x0000 is written to x7811F6

